

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

<b>Applicants:</b>	Takashi Nakano, et al.	<b>Examiner:</b>	R. Potter
<b>Serial No.:</b>	10/518,157	<b>Art Unit:</b>	2822
<b>Filed:</b>	December 10, 2004	<b>Docket:</b>	18497
<b>For:</b>	SEMICONDUCTOR DEVICE, SEMICONDUCTOR CIRCUIT AND METHOD OF FABRICATING SEMICONDUCTOR DEVICE	<b>Dated:</b>	April 12, 2007

**Confirmation No.:** 3521

Mail Stop Amendment  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

**AMENDMENT UNDER 37 C.F.R. §1.111**

Sir:

Applicants submit this Amendment for entry thereof in response to the Office

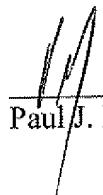
Action mailed January 12, 2007.

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**CERTIFICATION OF ELECTRONIC TRANSMISSION**

I hereby certify that this paper is being electronically transmitted to the Patent and Trademark Office on the date shown below.

Dated: April 12, 2007

  
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Paul J. Esatto Jr.